In the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A communication control device that controls data communication between a host computer and a peripheral device, comprising:

first buffer memory that stores data to be sent to the host computer;

second buffer memory that stores data outputted from the peripheral device; and

a control unit that is operable to generate a resume signal that is output from the peripheral device to the host, which is operable to generate a transmission approval command that is transferred to the peripheral device,

wherein the control unit is operable to transfer the data stored in the second buffer memory to the first buffer memory after receiving the transmission approval command from the host approving data transmission from the peripheral device to the host computer,

wherein the control unit is operable to transmit data from the first buffer memory to the host computer only after the transmission approval command is received from the host in response to the resume signal.

wherein the first buffer memory and the second buffer memory are FIFO buffers.

2. (Cancelled)

3. (Original) The communication control device according to claim 1, wherein the second buffer memory is a multi-stage FIFO buffer.

4. (Original) The communication control device according to claim 1, wherein the data communication is performed using a USB line.

5. (Previously presented) The communication control device according to claim 1, wherein the peripheral device includes the first buffer memory and the second buffer memory.